# Refine Search

#### Search Results -

Term	Documents
(16 AND 8).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	16
(L16 AND L8).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	16

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### Search History

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Set Name side by side	Query	<u>Hit</u> Count	Set Name result set
DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L18</u>	L16 and 18	16	<u>L18</u>
<u>L17</u>	L16 and l3	18	<u>L17</u>
<u>L16</u>	112 and 11	30	<u>L16</u>
<u>L15</u>	L12 and 18	283	<u>L15</u>
<u>L14</u>	L12 and l6	168	<u>L14</u>
<u>L13</u>	L12 and l3	367	<u>L13</u>
<u>L12</u>	(array\$1 or matrix or matrices) near12 (simd or single near1 instruction)	937	<u>L12</u>
DB=	PGPB,USPT; PLUR=YES; OP=OR		
<u>L11</u>	17 and 18	8	<u>L11</u>
<u>L10</u>	17 and 16	. 5	<u>L10</u>
<u>L9</u>	17 and 13	23	<u>L9</u>

<u>L8</u>	(712/2-22)![CCLS]	1613	<u>L8</u>
DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L7</u>	array\$1 and 15	57	<u>L7</u>
DB=	PGPB,USPT; PLUR=YES; OP=OR		
<u>L6</u>	(712/20-22)[CCLS]	400	<u>L6</u>
DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L5</u>	L4 and 12	57	<u>L5</u>
<u>L4</u>	(simd or single near 1 instruction)	10407	<u>L4</u>
DB=	PGPB,USPT; PLUR=YES; OP=OR		
<u>L3</u>	(712/2-300)[CCLS]	11094	<u>L3</u>
DB=	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L2</u>	(part\$9 or division\$1 or divid\$4 or split\$5 or break\$6 or broken or segment\$6 or portion\$1 or slic\$5 or chunck\$3) near8 packet\$1 near25 (pe\$1 or processing\$1 near2 element\$1)	1534	<u>L2</u>
<u>L1</u>	(part\$9 or division\$1 or divid\$4 or split\$5 or break\$6 or broken or segment\$6 or portion\$1) near8 packet\$1	45470	<u>L1</u>

## END OF SEARCH HISTORY



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iees sto	IEEE Standard	П	<ol> <li>Horizontal architecture for high performance parallel communication systems         Pyeong Jung Kim; Sang Hwan Kung; Seung Ku Hwang;         High Performance Computing on the Information Superhighway, 1997. HPC Asia '97     </li> <li>28 April-2 May 1997 Page(s):472 - 477</li> </ol>			
			AbstractPlus   Full Text: PDE(480 KB)   IEEE CNF			
		D	<ol> <li>TCP-friendly SIMD congestion control and its convergence behavior Shudong Jin; Liang Guo; Matta, I.; Bestavros, A.; Network Protocols, 2001. Ninth International Conference on 11-14 Nov. 2001 Page(s): 156 - 164</li> </ol>			
			AbstractPlus   Full Text: PDE(845 KB) IEEE CNF			
			3. Design of SIMD microprocessor array  Jesshope, C.R.; O'Gorman, R.; Stewart, J.M.;  Computers and Digital Techniques, IEE Proceedings-  Volume 136, Issue 3, May 1989 Page(s):197 - 204			
			AbstractPlus   Full Text: PDF(792 KB) 《経管 JNL			
		D	<ol> <li>A journey into multicomputer routing algorithms</li> <li>Grammatikakis, M.D.; Hsu, D.F.; Kraetzl, M.;</li> <li>Parallel Algorithms/Architecture Synthesis, 1995. Proceedings. First Aizu International Symposium on 15-17 March 1995 Page(s):19 - 27</li> </ol>			
			AbstractPlus   Full Text: PDF(828 KB) IEEE CNF			
		<u>.</u>	<ol> <li>Translucent smart pixel array (TRANSPAR) chips for high throughput networks and SIMD signal proceeding. CH.; Hoanca, B.; Kuznia, C.B.; Sawchuk, A.A.; Wu, JM.;</li> <li>Selected Topics in Quantum Electronics, IEEE Journal of Volume 5, Issue 2, March-April 1999 Page(s):316 - 329</li> </ol>			
			AbstractPlus   References   Full Text: PDE(776 KB)   IEEE JNL			
		D	<ol> <li>Routing permutations on MESH interconnection networks         Sibeyn, J.F.;         Parallel and Distributed Processing, 1990. Proceedings of the Second IEEE Symposium on 9-13 Dec. 1990 Page(s):94 - 97     </li> </ol>			
			AbstractPlus   Full Text: PDF(316 KB) IEEE CNF			

	Parallel, Distributed and Network-based Processing, 2002. Proceedings. 10th Euromicro Workshop on
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